Remarks

Entry of the foregoing Preliminary amendment into the above-identified Application, prior to examination thereof, is respectfully requested.
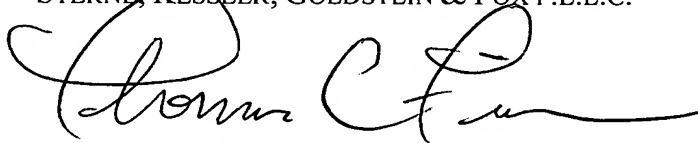
Applicants have made minor corrections to the specification. These corrections introduce no new matter, and their entry is respectfully requested.

Upon entry of the foregoing amendment, claims 12-20 are pending in the application, with claim 12 being the independent claim. Claims 1-11 are sought to be cancelled without prejudice to or disclaimer of the subject matter therein. New claims 12-20 are sought to be added. These changes are believed to introduce no new matter.

The Examiner is invited to telephone the undersigned representative if an interview is believed to be useful for any reason.

Respectfully submitted,

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Version with markings to show changes made

In the Specification:

Paragraph [0005] has been amended as follows:

A major consideration in a superscalar RISC processor is how to execute multiple instructions in parallel and out-of-order, without incurring data errors due to dependencies inherent in such execution. Data dependency checking, register renaming and instruction scheduling are integral aspects of the solution. A detailed discussion of storage conflicts, register renaming and dependency mechanisms is found in [commonly owned, co-pending] related U.S. Patent No. 5,497,499 to Garg *et al.* (hereinafter referred to as the '499 patent).

Paragraph [0053] has been amended as follows:

MONDEP 808 determines which dependencies have disappeared by comparing the tags of retiring or recently-retired instructions with the lower three bits of the renamed sources of each instruction. Information regarding retired [instructons] instructions is sent to MONDEP 808 via a bus 828 from a retirement unit (not shown; the details of a retirement unit that can be used to generate these signals is disclosed in [co-pending, commonly owned] related U.S. Patent No. 5,826,055 to Wang *et al.*). If there is a match, then MONDEP 808 knows that the dependency has been removed and the outputs of MONDEP 808 outputs which instructions' inputs have been moved from the temp[,] buffer to the register file. These output signals are sent to RFMXING 806 and RDY_GEN 810 via buses 822.

In the Claims:

Claims 1-11 were canceled.

New claims 12-20 were added.

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